

Abstracts

Monolithic Optoelectronic Receiver for Gbit Operation

S. Ray and M.P. Walton. "Monolithic Optoelectronic Receiver for Gbit Operation." 1986 *Microwave and Millimeter-Wave Monolithic Circuits Symposium Digest 86.1* (1986 [MCS]): 39-40.

A monolithic optoelectronic receiver chip was designed and fabricated for operation at 1.0 Gbit/sec data transmission rate. The major components of the receiver chip are a fast photodiode, a preamplifier, and a 1:4 demultiplexer circuit fabricated on semi-insulating GaAs substrate. Optical/electrical functionality has been demonstrated at wafer level.

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